

SURFACE MOUNT PACKAGE WITH INTEGRAL ELECTRO-STATIC CHARGE
DISSIPATING RING USING LEAD FRAME AS ESD DEVICE

ABSTRACT OF THE DISCLOSURE

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In a packaged integrated circuit, electrostatic discharge protection is provided by portions of a lead frame on which the integrated circuit is mounted. The lead frame includes a die paddle on which an integrated circuit die is mounted, with plastic or epoxy material encapsulating exposed surfaces of the integrated circuit die except for a sensing surface, and supporting pins or leads formed from the lead frame. Portions of the lead frame extending from the die paddle are folded around sides of the encapsulated integrated circuit die and over, or adjacent to and level with, a peripheral upper surface of the encapsulated integrated circuit die to form an electrostatic discharge ring. The lead frame portions folded around the integrated circuit package are connected to ground through a ground pin, so that charge on a human finger touching the electrostatic discharge ring is dissipated to ground before the finger contacts a sensing surface of the integrated circuit. The portions of the lead frame which are folded around the encapsulated integrated circuit die may extend only around sides or side regions of the integrated circuit package not including pins or leads or, alternatively, may extend around all sides of the integrated circuit package and have openings where side regions of the integrated circuit package includes pins or leads.